

# High Performance Closed-Channel Cooling System Using Multi-channel Electro-osmotic Flow pumps for 3D-ICs

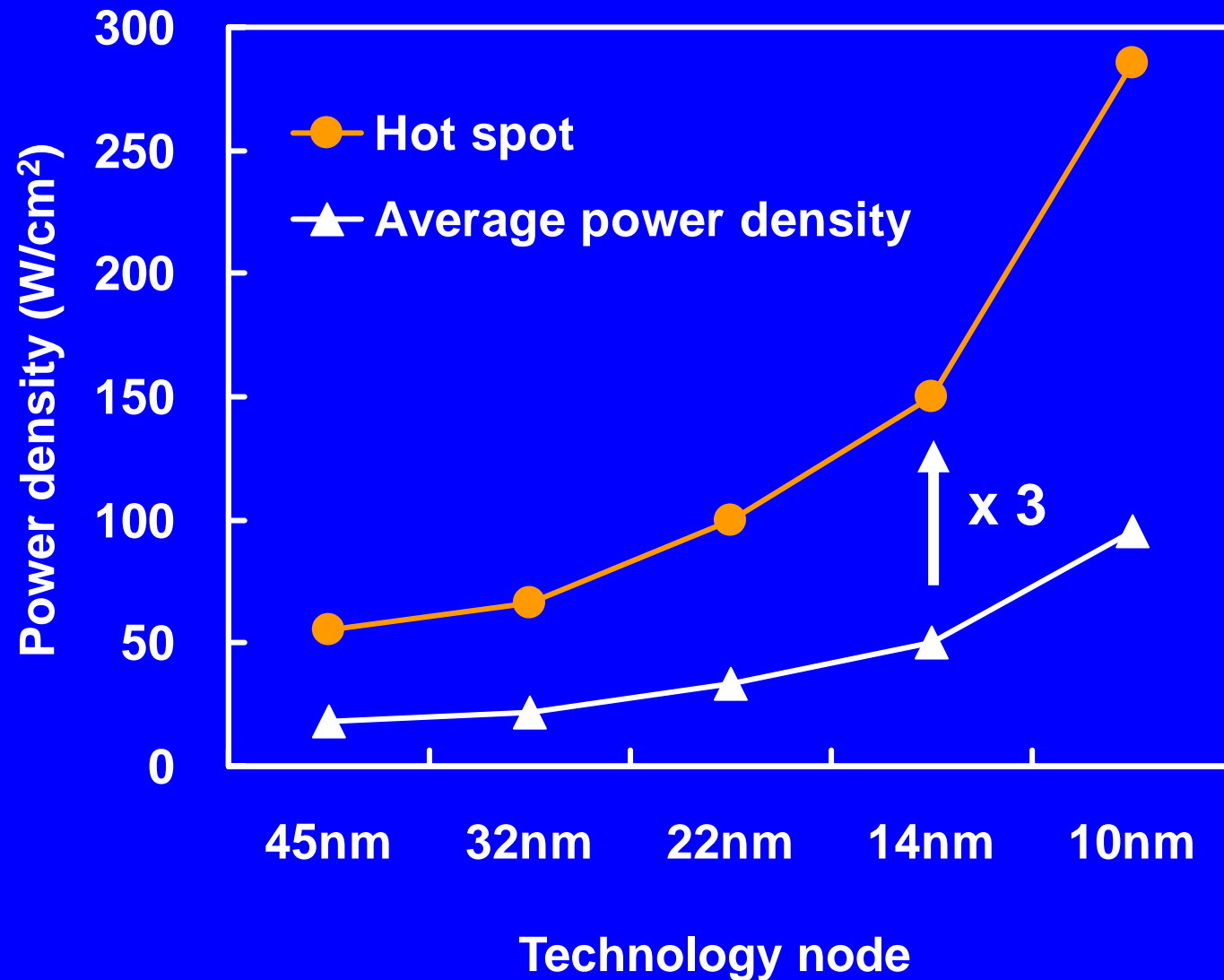
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# Outline of Presentation

- **Closed-channel cooling system**
- **Multi-channel EOF pumps**
- **Cooling chip fabrication**
- **Pumping performance**
- **Cooling capability**

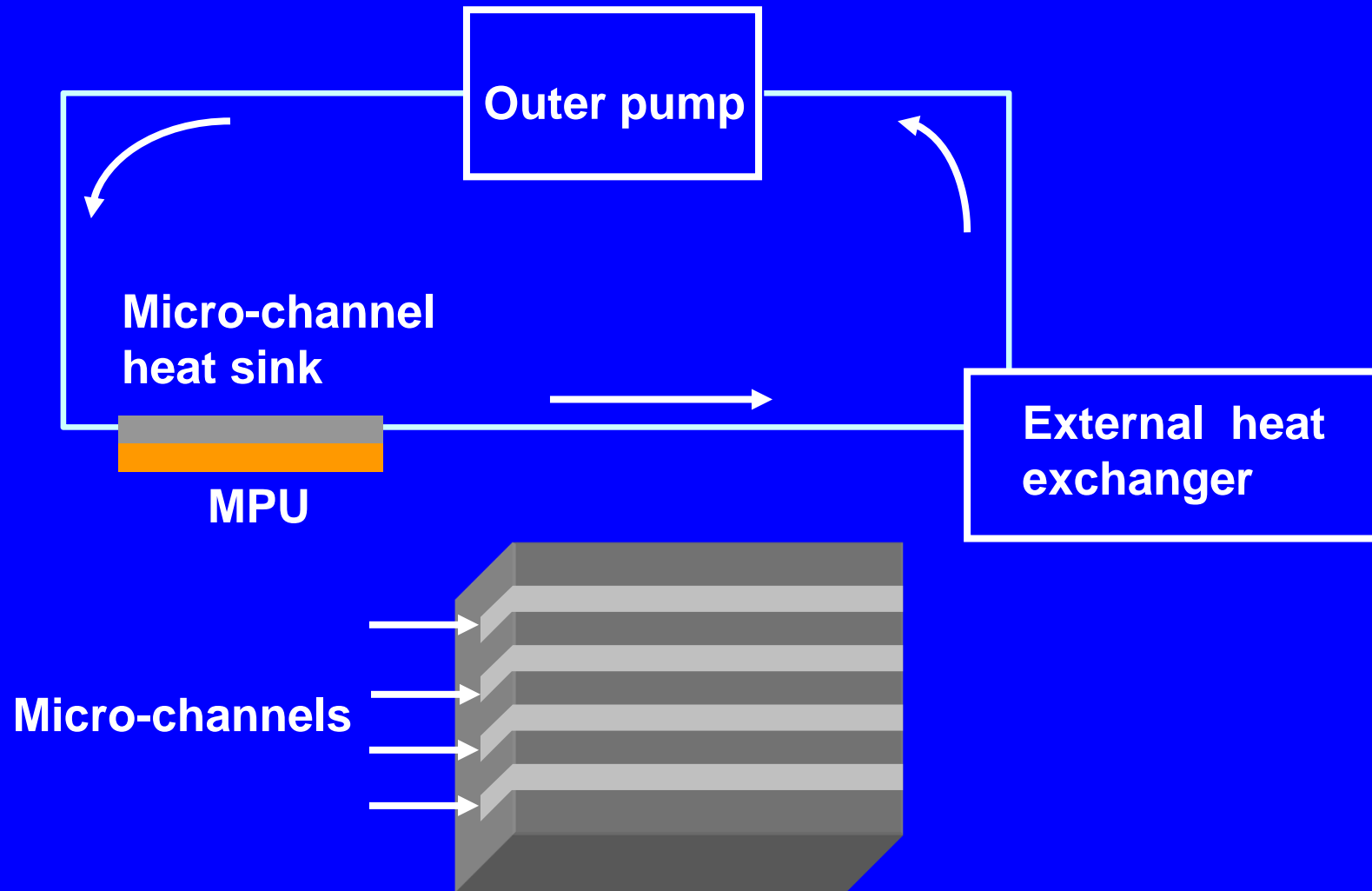
# Power Density of MPUs



# Hot Spot Issues

- Lowering of speed performance
- Increase in power consumption
- Degradation of functional reliabilities
- **Thermal management for 3D-IC integration**

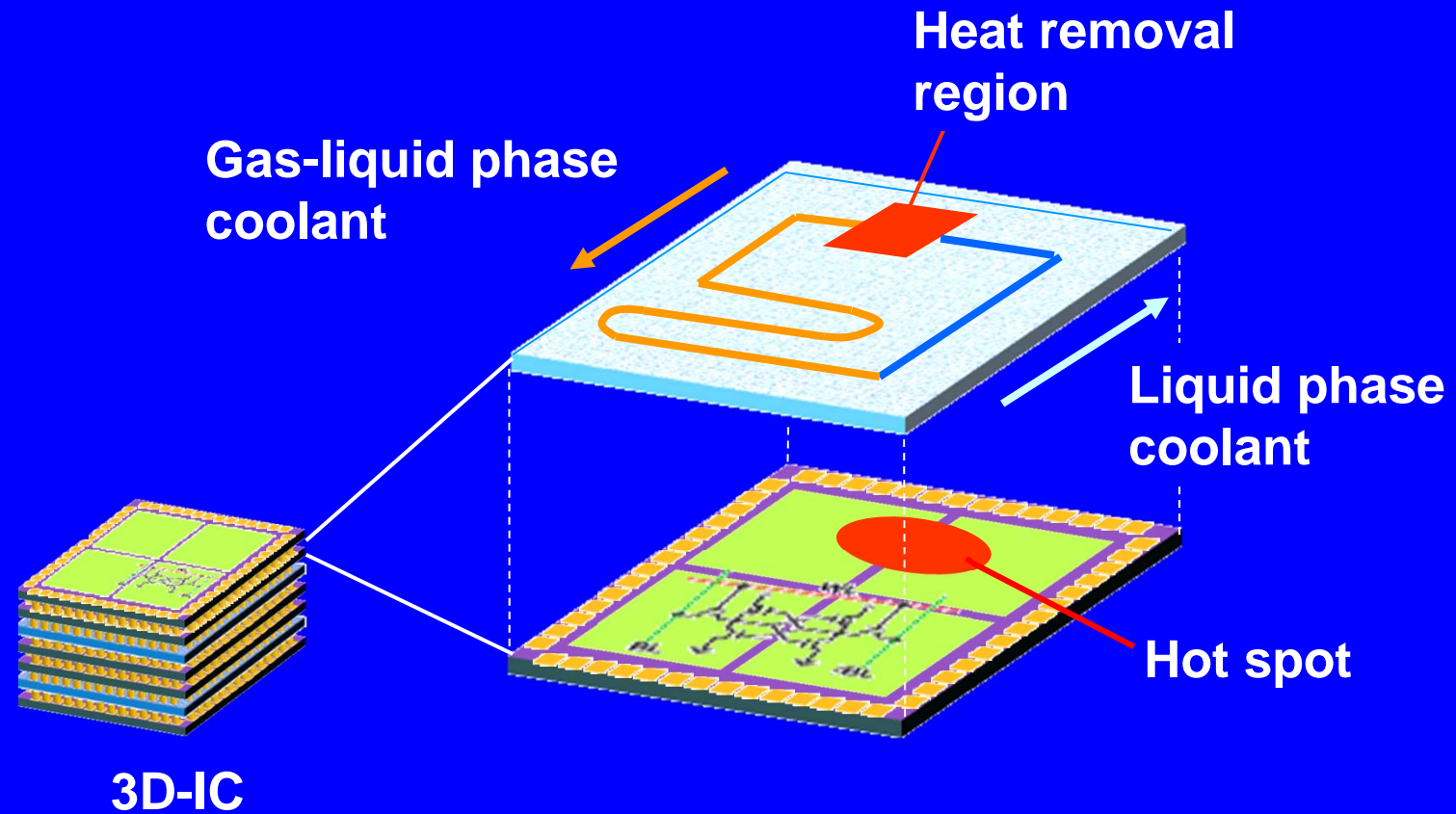
# Typical Micro-channel Cooling System



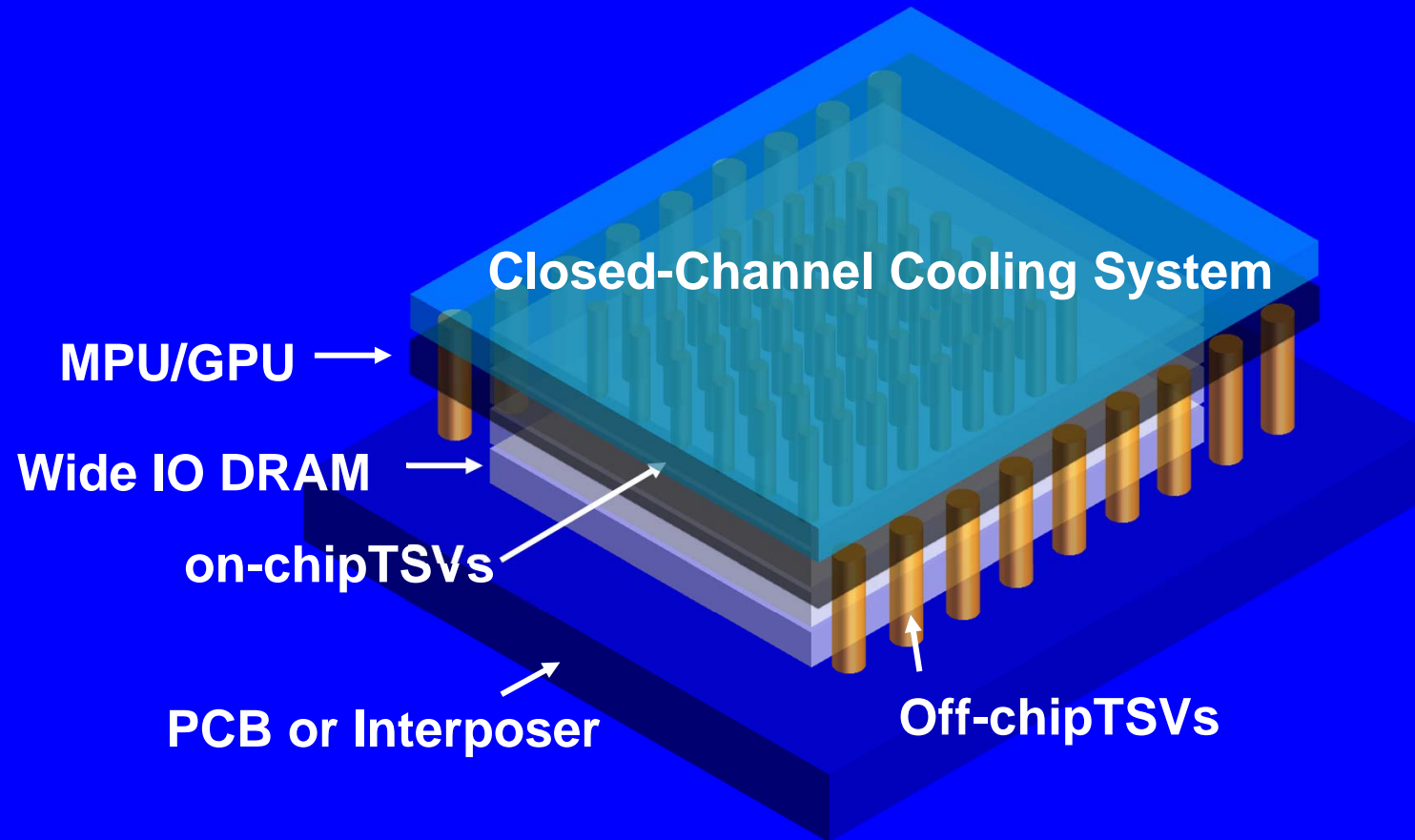
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# Closed-channel Cooling System (C<sup>3</sup>S)



# Application of C<sup>3</sup>S to 3D-IC

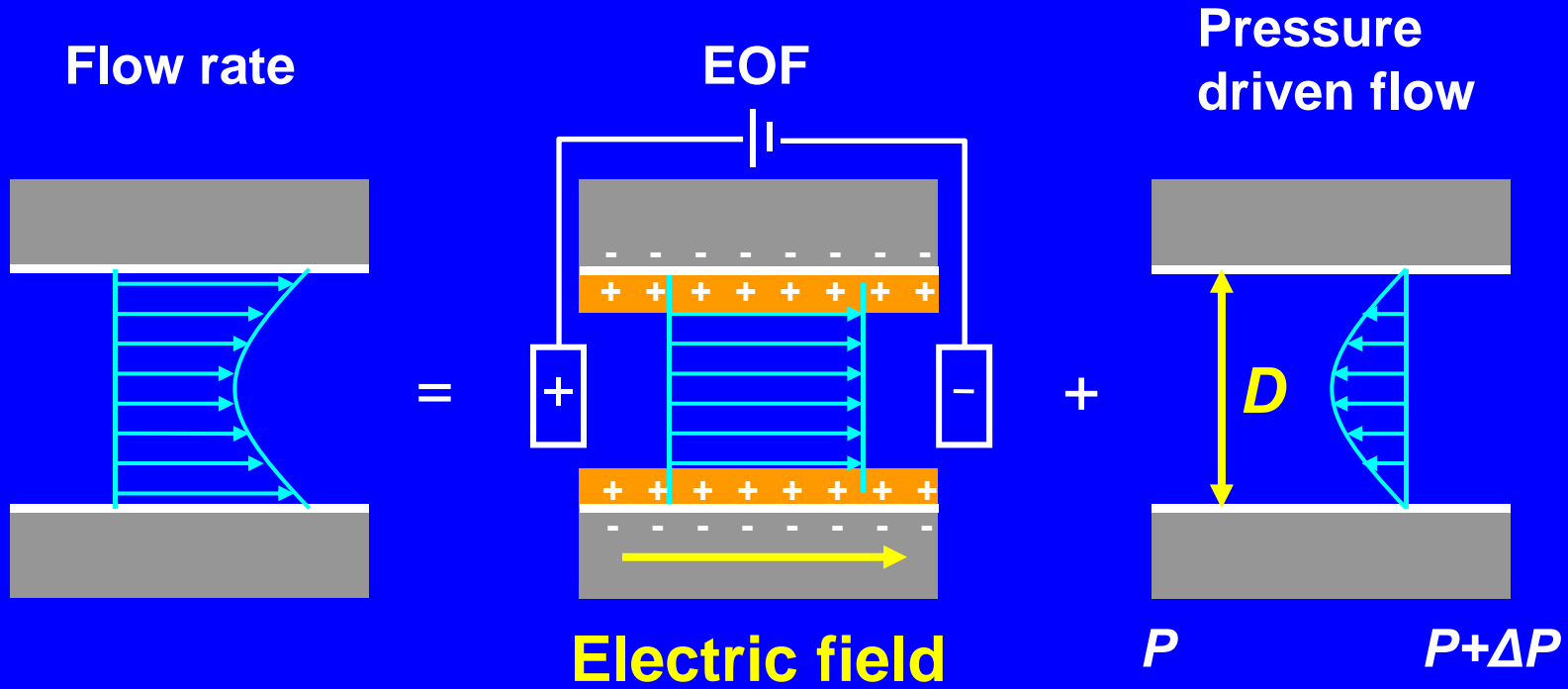




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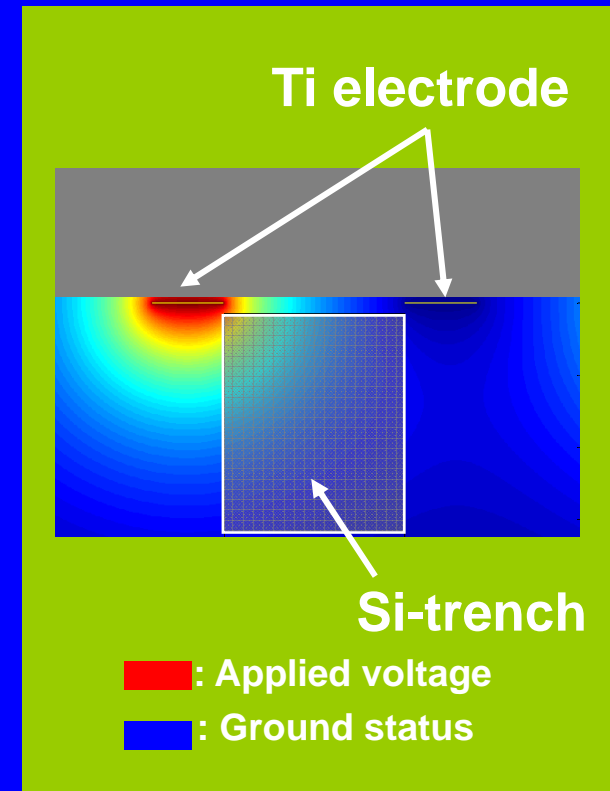
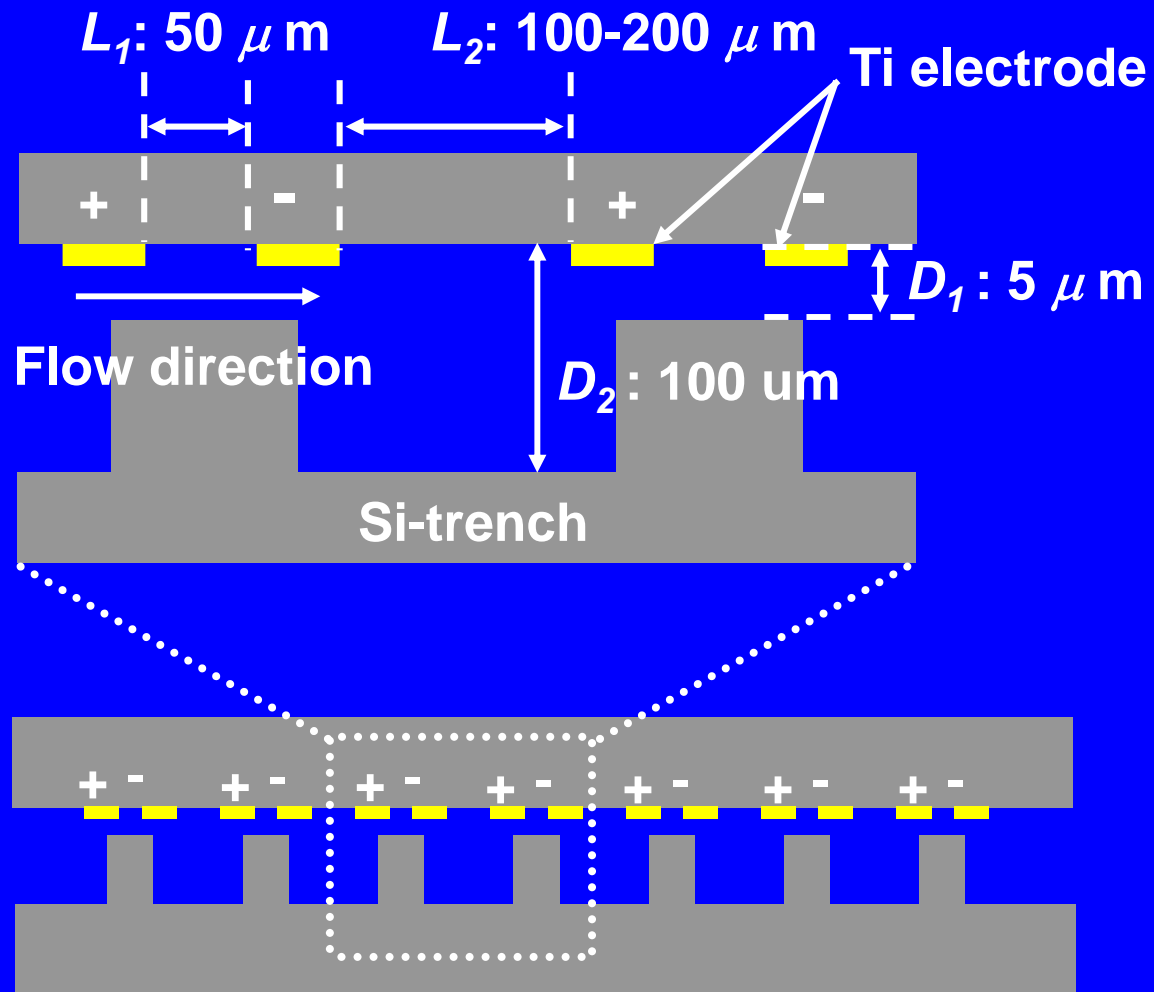
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# Electro-osmotic Flow (EOF) Mechanism



$$\text{Flow rate} = - \frac{\epsilon \zeta w D}{\mu} E - \frac{w D^3}{12\mu} \frac{\Delta P}{L}$$

# Multi-channel EOF pumps



# Dimensions of EOF pump

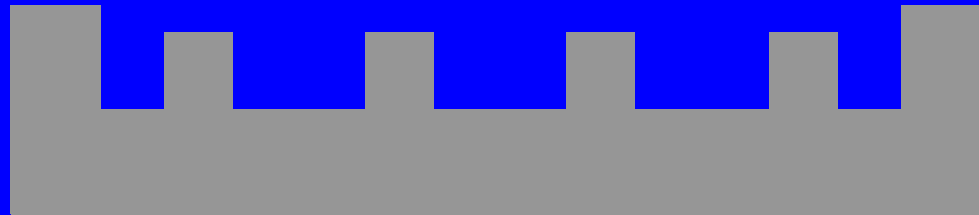
| Chip                            | Channel-A | Channel-B | Channel-C |
|---------------------------------|-----------|-----------|-----------|
| $D_1$ ( $\mu\text{m}$ )         | 5         | 5         | 5         |
| $D_2$ ( $\mu\text{m}$ )         | 100       | 100       | 100       |
| $L_1$ ( $\mu\text{m}$ )         | 50        | 50        | 50        |
| $L_2$ ( $\mu\text{m}$ )         | 100       | 200       | 200       |
| Channel<br>(EOF unit)<br>number | 91        | 55        | 46        |

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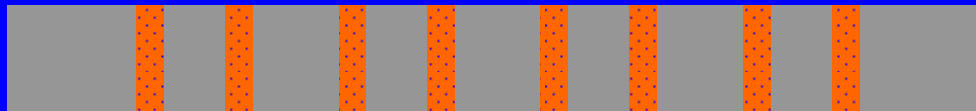
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# Fabrication Process of Cooling Chip

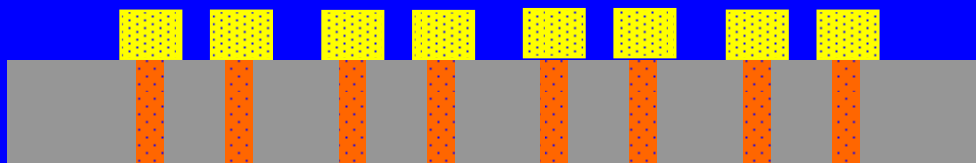
(a) Si trench formation



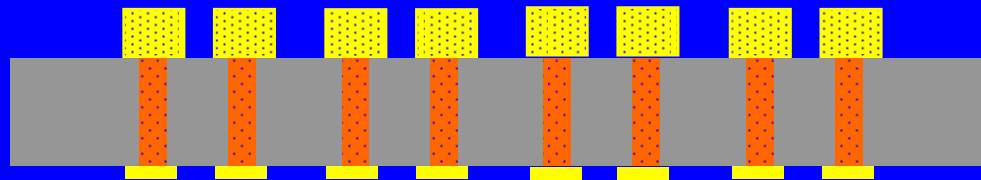
(b) Formation of through Si via (TSV) filled with electro-plated Cu



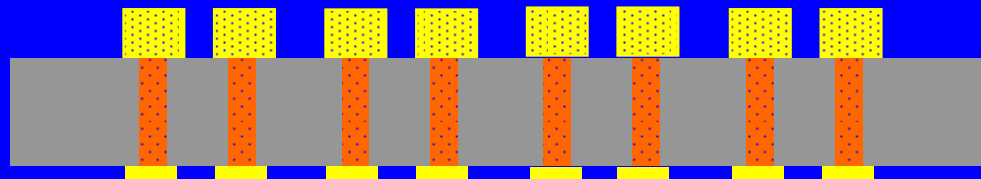
(c) Patterning Al line for applying voltage



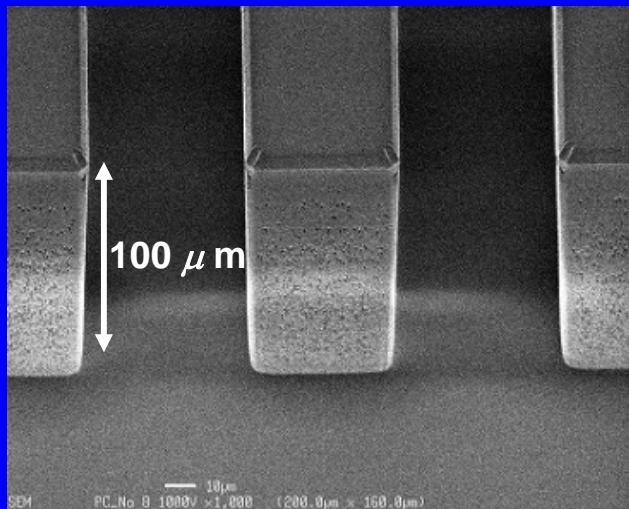
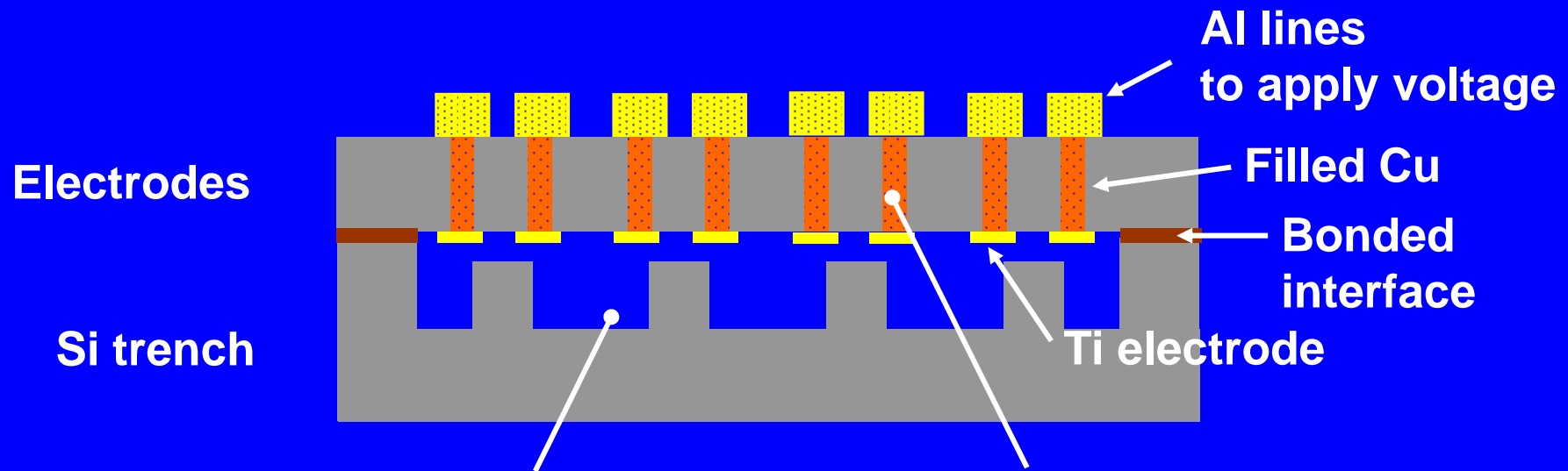
**(d) Patterning of Ti electrode for EOF pumps**



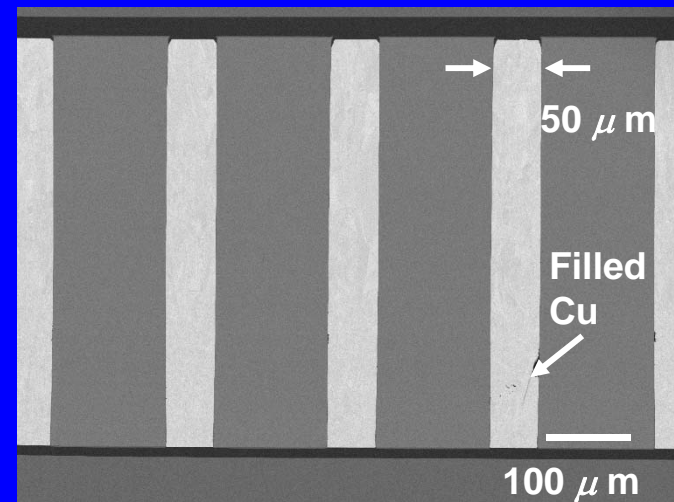
**(e) Surface activated bonding (SAB)**



# In-process SEM photos



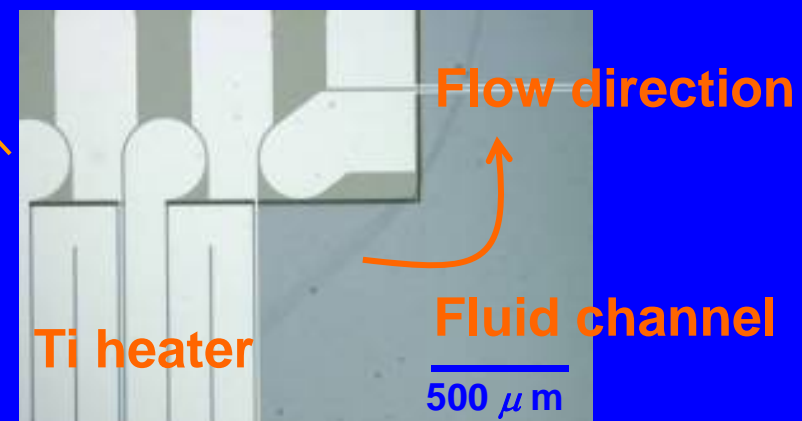
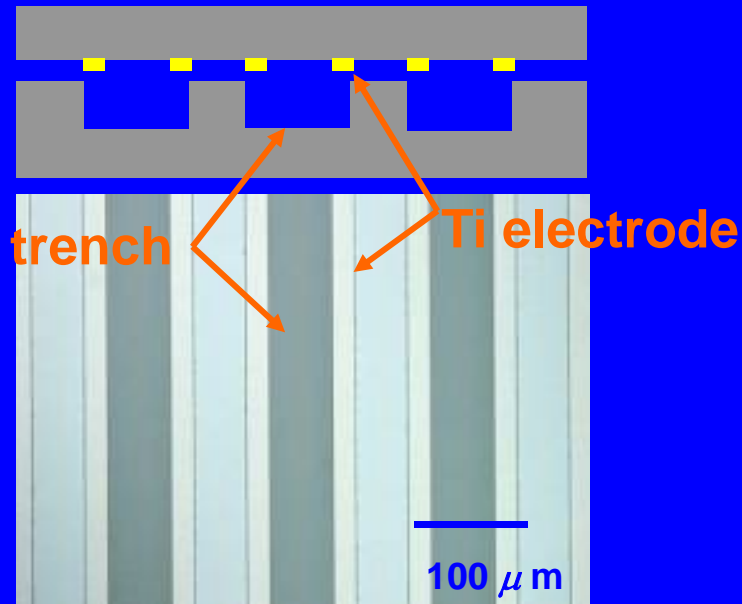
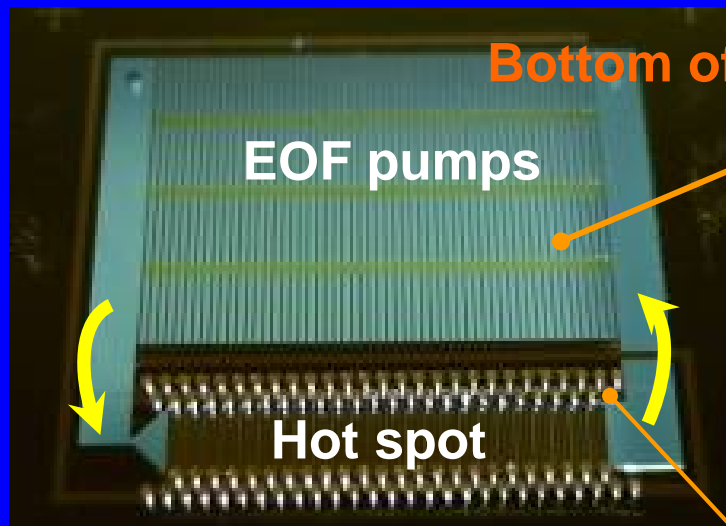
Si trench from tilt angle



TSV filled with electro-plated Cu

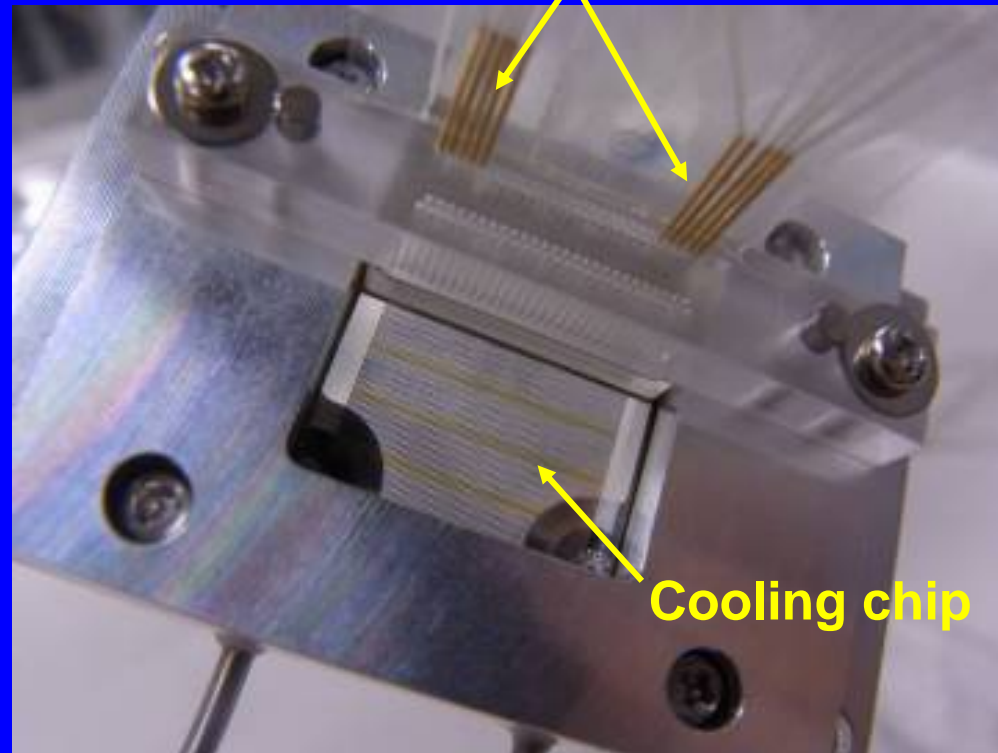


# Close-up Images of EOF pumps and Hot Spot



# Cooling Chip Mounted to Measurement Jig

Wires for applying voltage



Cooling chip

Inlet

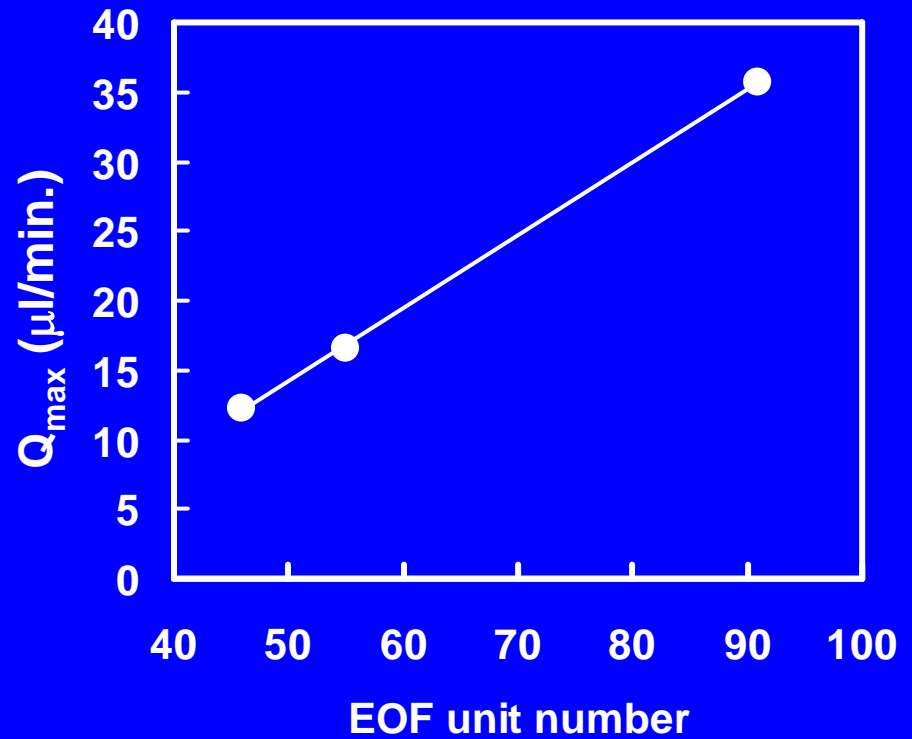
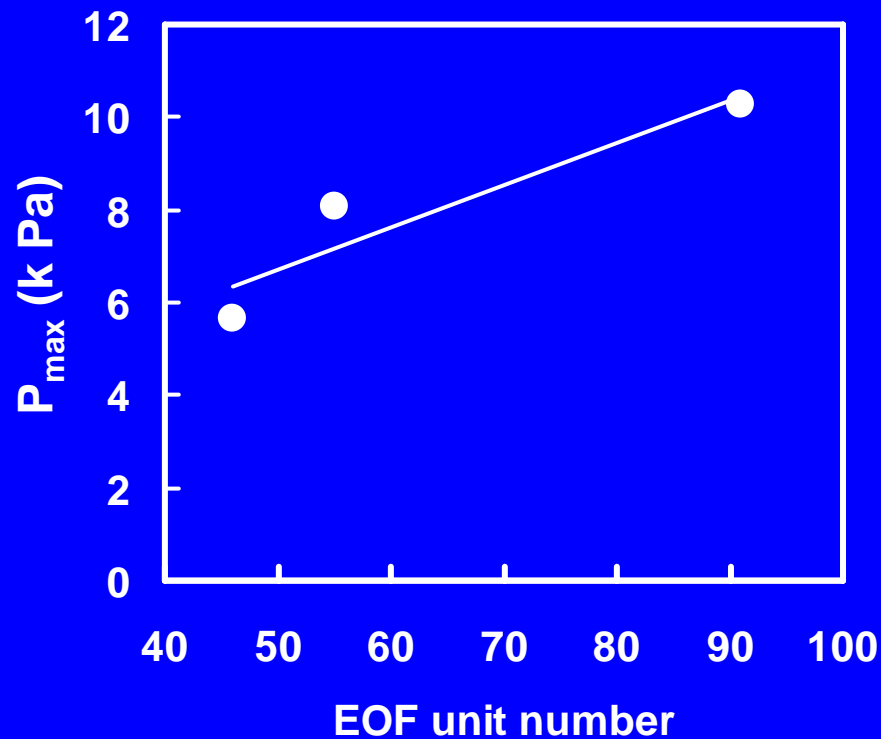
Outlet

Coolant: Isopropyl alcohol (IPA)

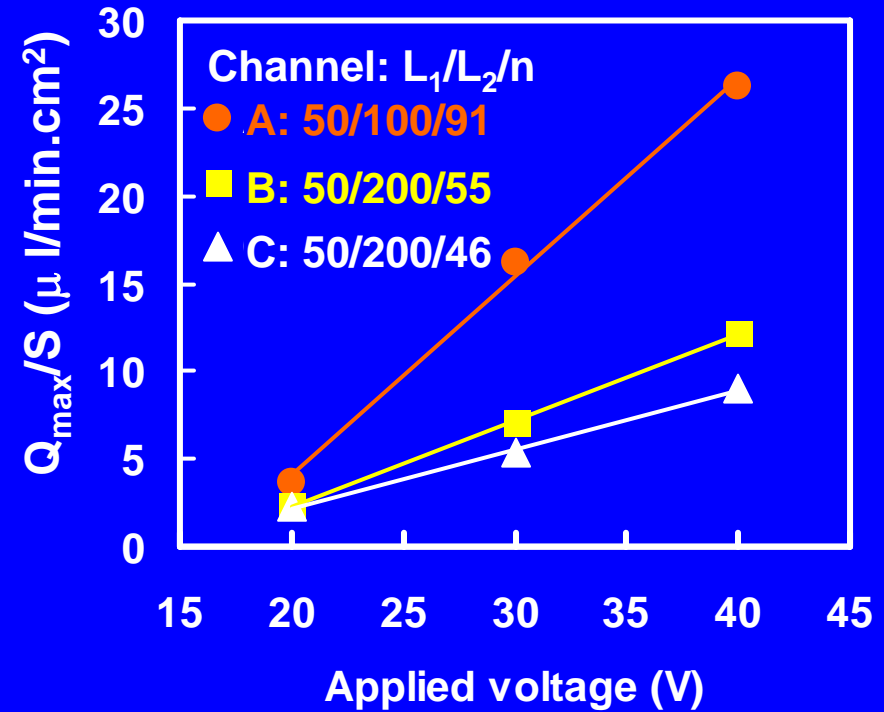
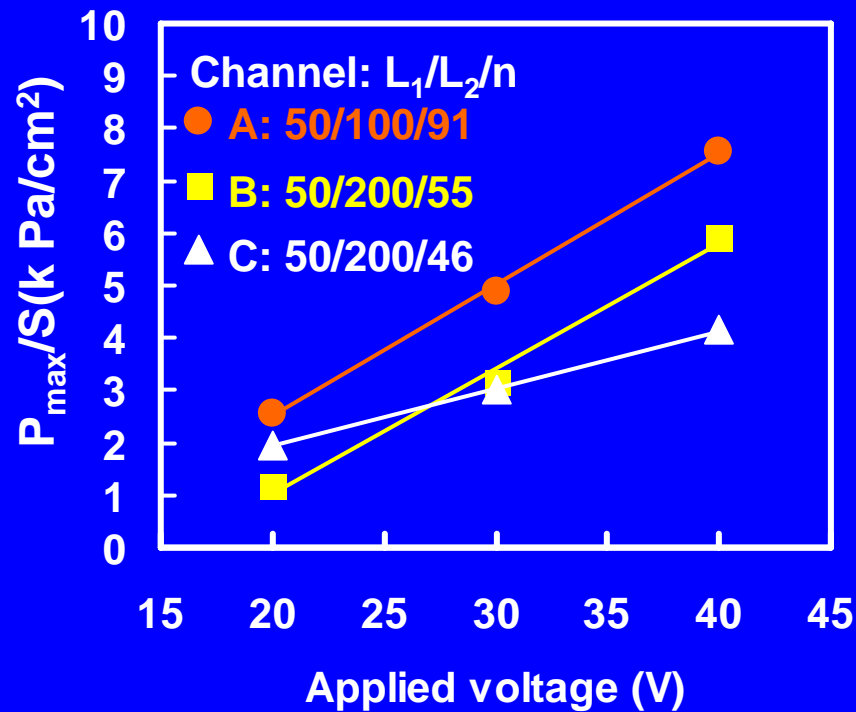
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# $P_{\max}$ and $Q_{\max}$ as a Function of Number of EOF Unit at 40 V



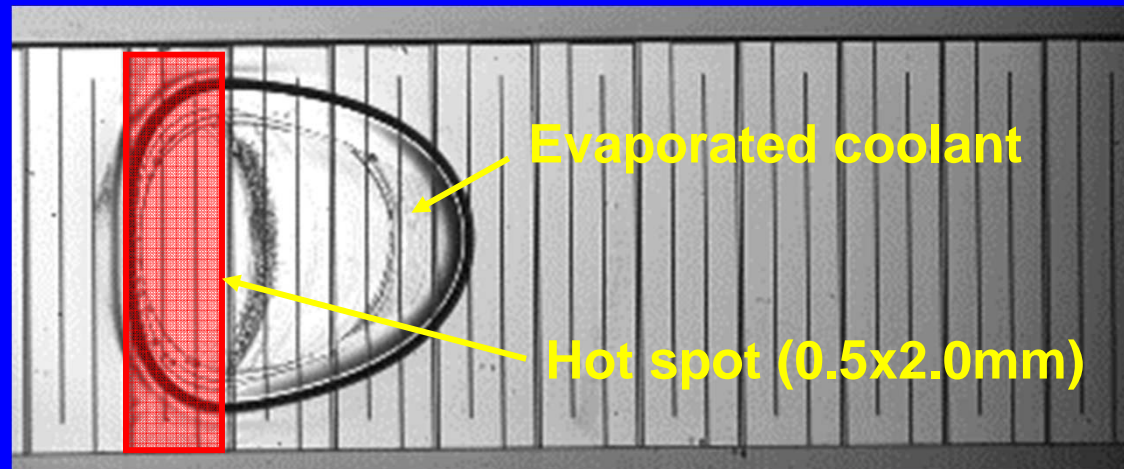
# Normalized $P_{\max}$ and $Q_{\max}$ by Total Channel Area



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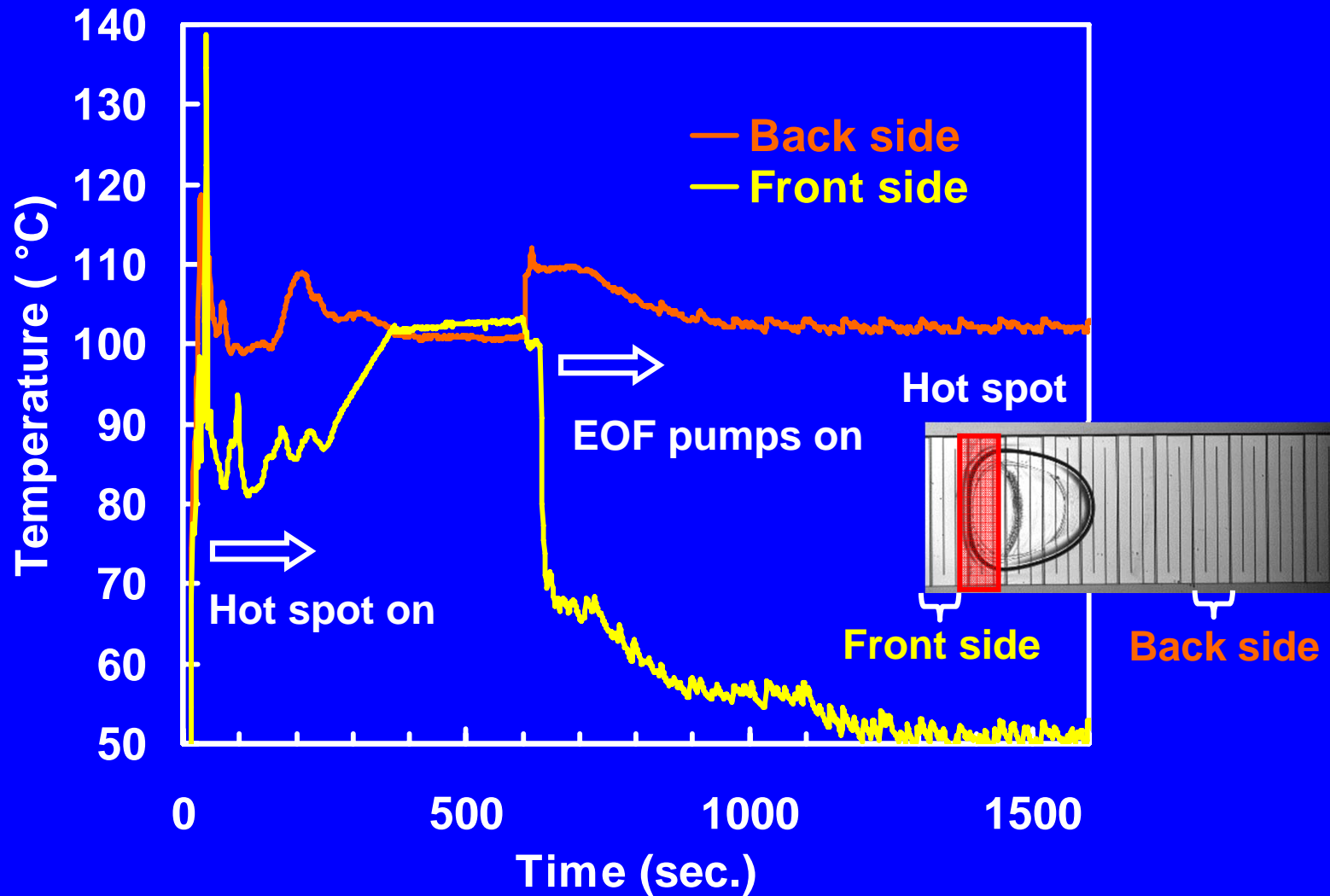
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# Fluid Behavior of Coolant on Hot Spot



Flow direction

# Change in Coolant Temperature at Back and Front Sides of Hot Spot





# Summary

- We have successfully fabricated a cooling chip equipped with C<sup>3</sup>S with multi-channel EOF pumps.
- Operating the EOF pumps with a very low applied voltage of 40 V.
- High pumping capability:  $P_{\max} = 10$  kPa,  $Q_{\max} = 38$   $\mu$ l/min.
- Cooling capability was as high as 140 W/cm<sup>2</sup>.
- C<sup>3</sup>S with the EOF pumps enable us to meet the requirements of 3D-ICs.

# Acknowledgements

**This study was carried out based on the three-dimensional integration development program of the WOW Alliance and the authors thank the more than 20 alliance members.**