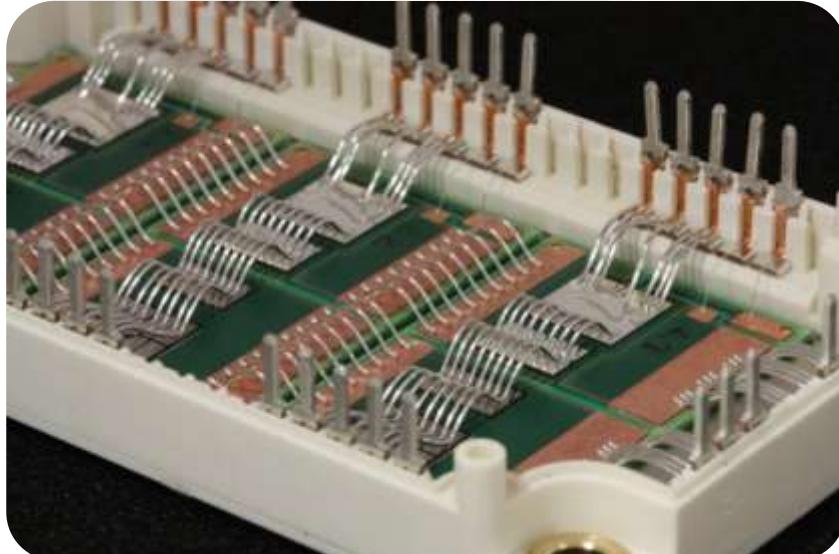


Panel Discussion ECTC 2012

Pioneering Innovative Packaging Technologies

Klaus-Dieter Lang



Global Challenges for Innovative Power Electronics

Energy Turnaround



Energy Efficiency



E-Mobility



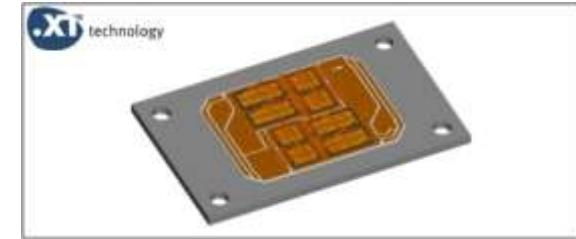
Automation/ Drives



Driving Forces for High Performance Packaging

Systemintegration

- Increasing performance and complexity ⇒ higher heat losses
- Different places of installation ⇒ high temperature environment
- Miniaturization ⇒ higher power density



Infineon: XT-Technology for Power Packages

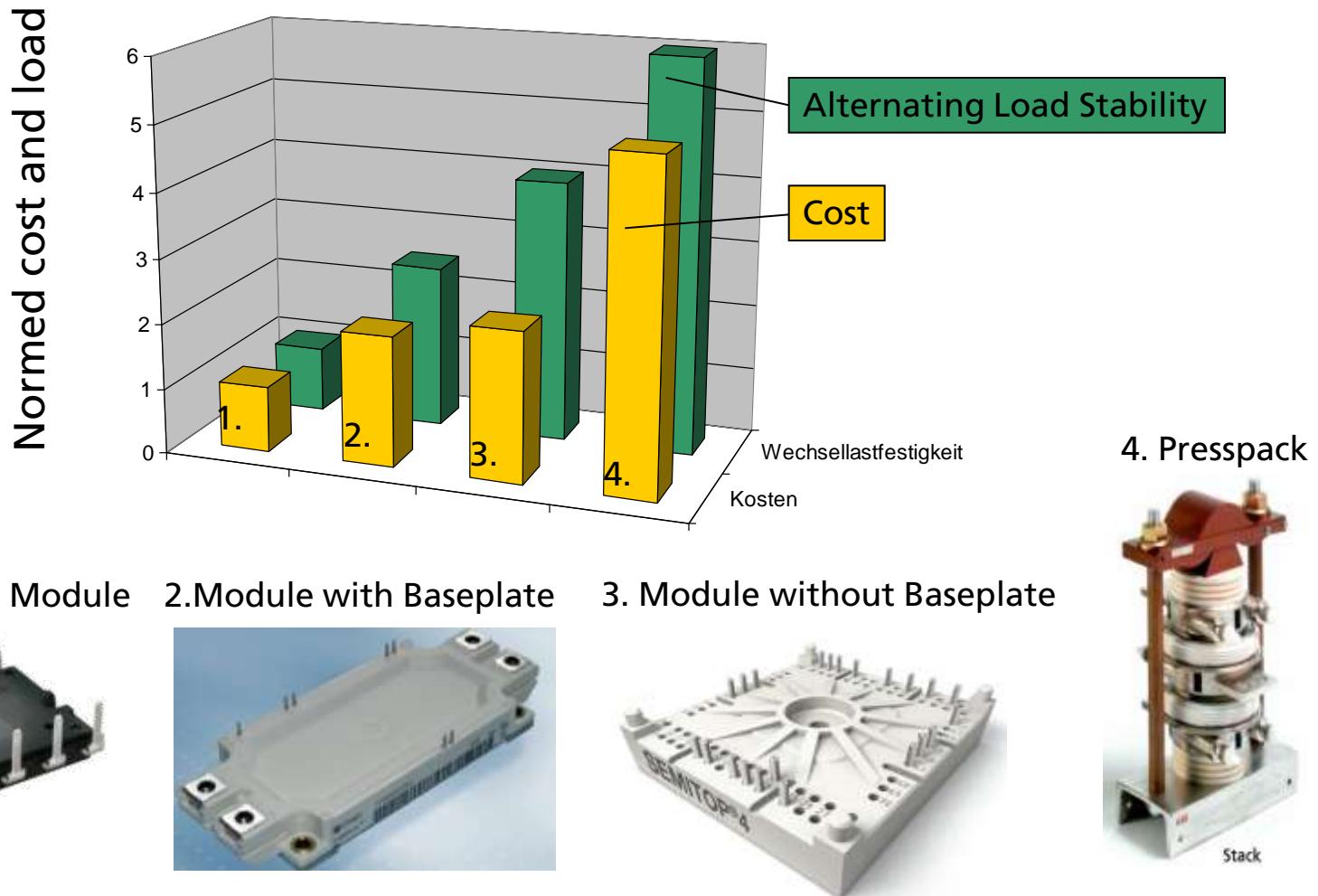
Manifold Applications

- Control Units
- Converter for eCar, Solar, ...
- LED-Systems
- Power Supplies
- Smart power electronics



Fraunhofer IZM: Sandwich Technology for Power Modules

Classification of Today's Packaging Solutions



Demands on Innovative Power Electronics

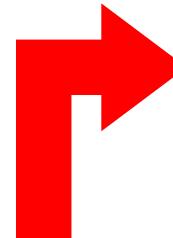
Electromagnetic Design

Handling of package parasitics to optimise switching behavior, e.g. low inductance and capacitance



Thermal Management

Heat Path Optimisation e.g.
by improved TIMS
Water cooling
Improved cooling concepts,
e.g. phase change double
sided



Demand for Pioneering Packaging Technologies

- Integration of Control and Power Electronics
 - Compact High Power Modules
 - Cost Efficient Technologies
 - Mechanical Interconnects

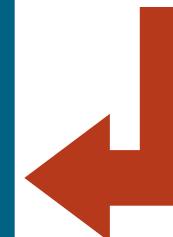
Assembly & Packaging Technologies

Cu-Wire, sintering, high performance soldering

PCB Embedding

Advanced substrates

High temperature packages



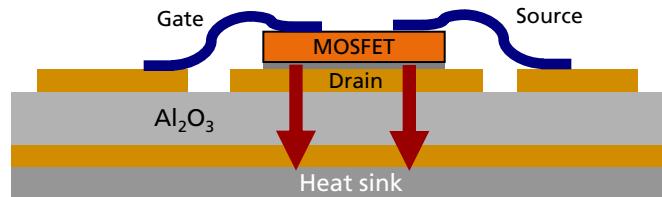
Reliability

Combined acceleration test
Condition monitoring
Live time modeling

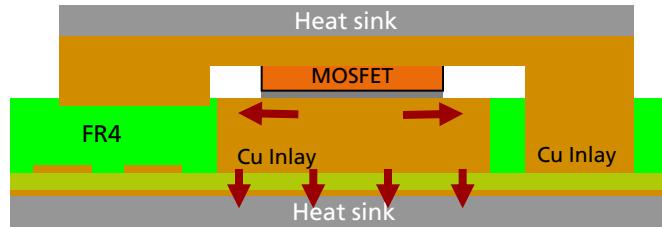
Assembly and Packaging Technologies

Assembly and Packaging Strategies

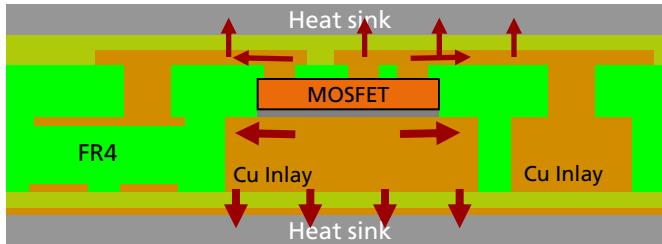
power chip wire-bonded
on DCB



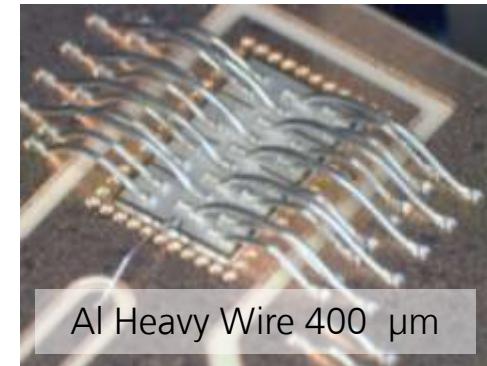
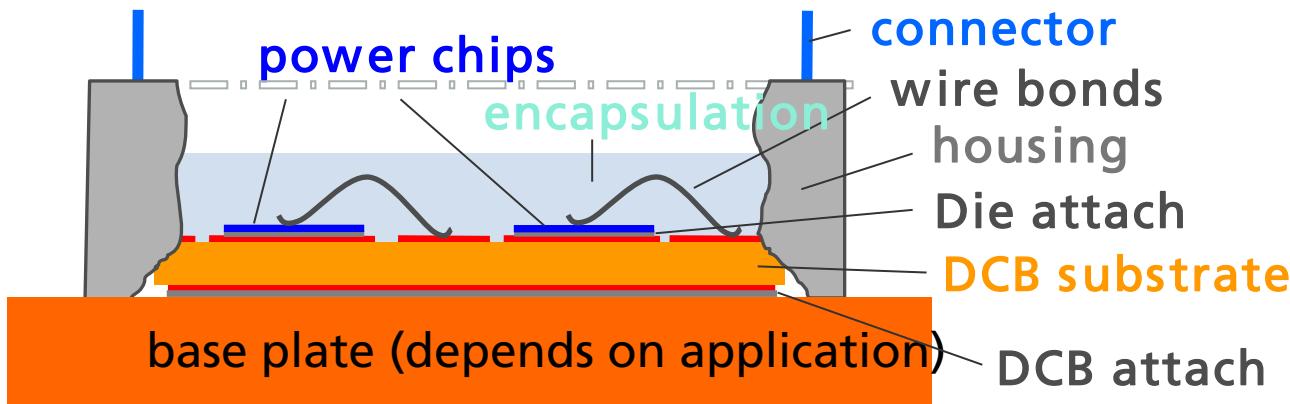
power chip double side
sandwich on PCB



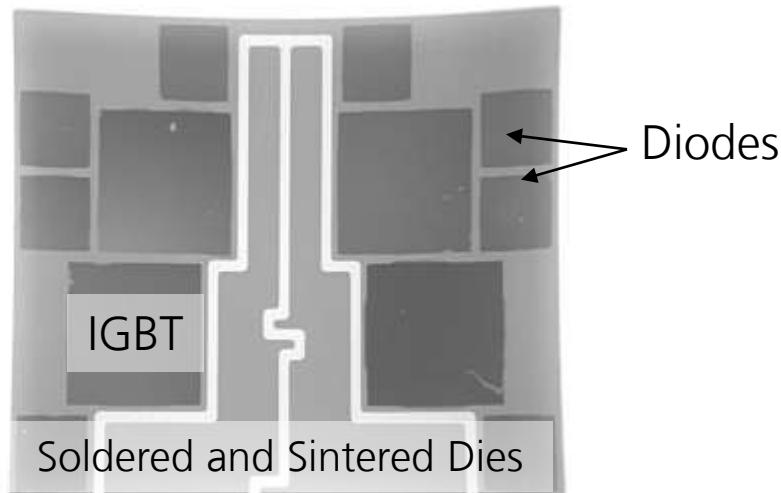
power chip embedded
into power PCB



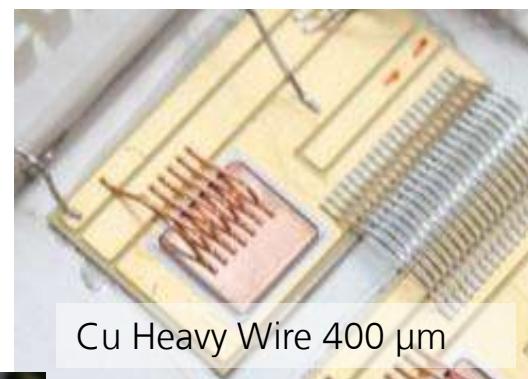
New Interconnection Technologies for Power Modules



Al Heavy Wire 400 µm



Al Ribbon



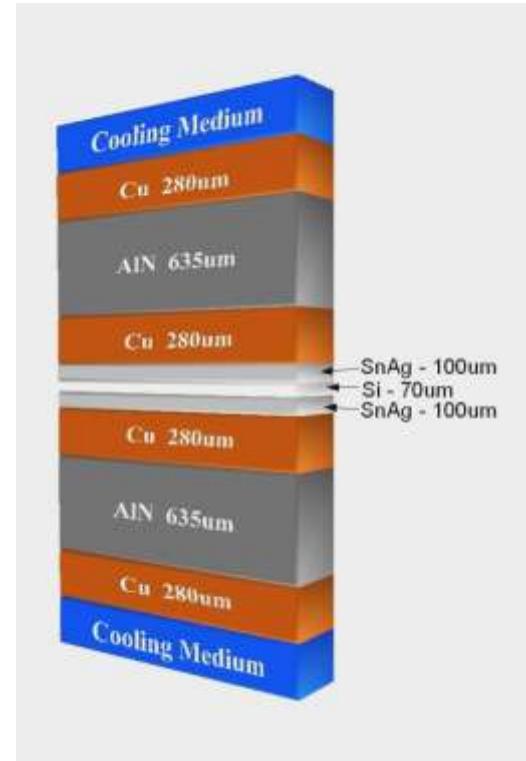
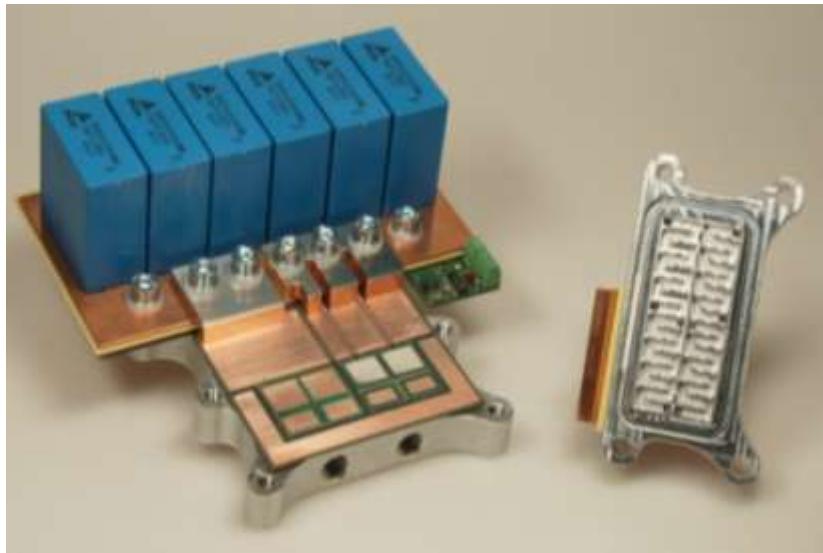
Cu Heavy Wire 400 µm



Cu/Al Ribbon

Advanced Chip and Wire Technologies

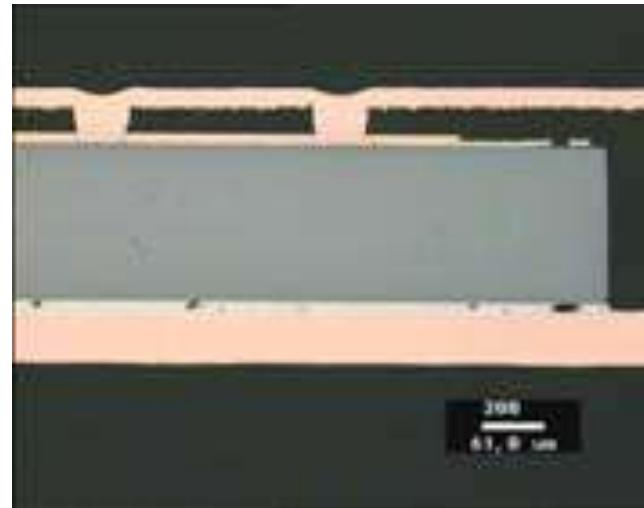
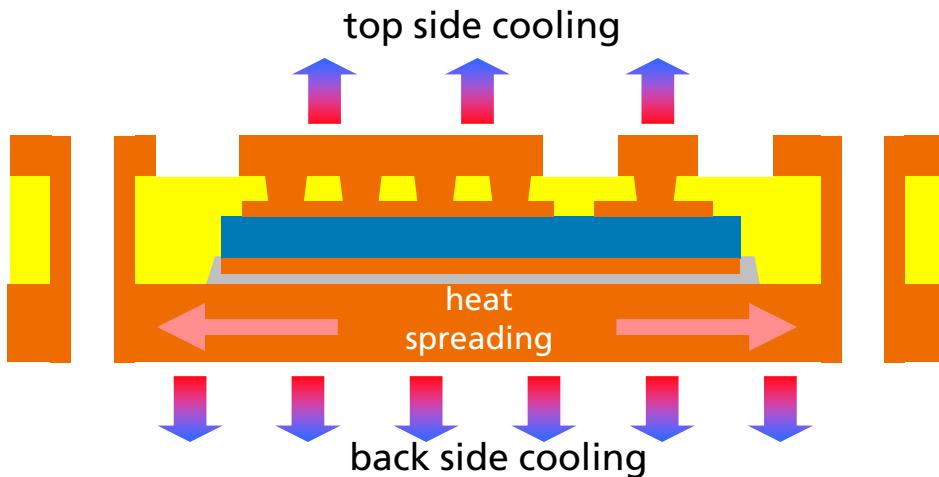
Sandwich Packaging with Double Side Cooling



- Package with very low thermal resistance due to short distance to heat sink and minimized number of interfaces
- Package for 500A peak, 600V
- Cooling by Cooling cycle combustion engine

Power Chip Embedding

- low inductances
- 3D packaging
- high reliability
- top and back side heat transfer
- shielding capability



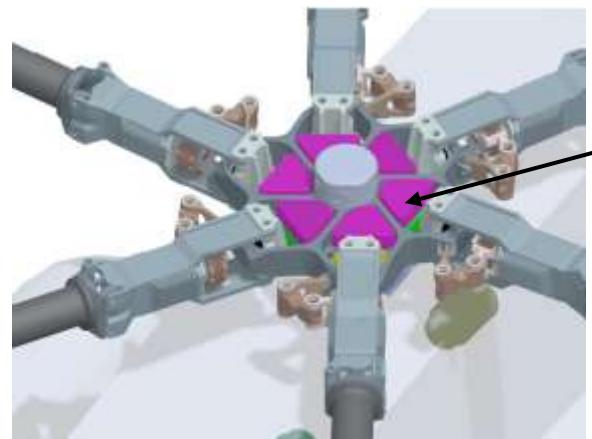
embedded MOSFET

Applications

Converter for Helicopter Rotor Blade Control



- Converter system for harsh environment
- High redundant, Volume < 3 litre, 270V, 44Aeff, rotation resistant, air cooling
- All parts airworthy
- Formfactor optimized, volume reduction, higher performance



Courtesy of ZF

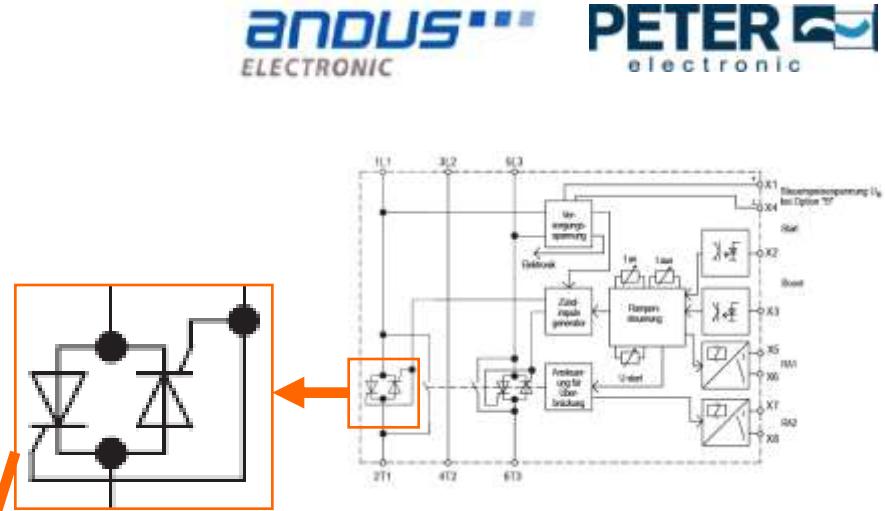
Thyristor Power Module

project goal

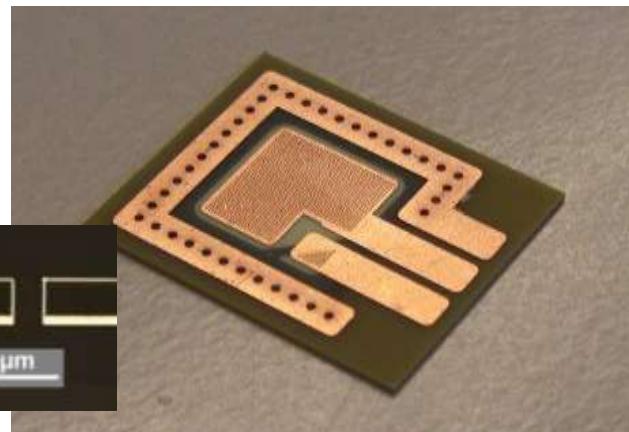
- soft starter for large motors
- phase angle control using of 4 thyristors
- 400 V, 45 A
- first test module
 - 1 embedded thyristor
 - Ag-filled adhesive
- final system
 - 4 embedded thyristors
 - die bonding by Ag sintering
 - PCB with 2 mm Cu core
 - attached to air cooler



cross-section test module



first test module with embedded thyristor

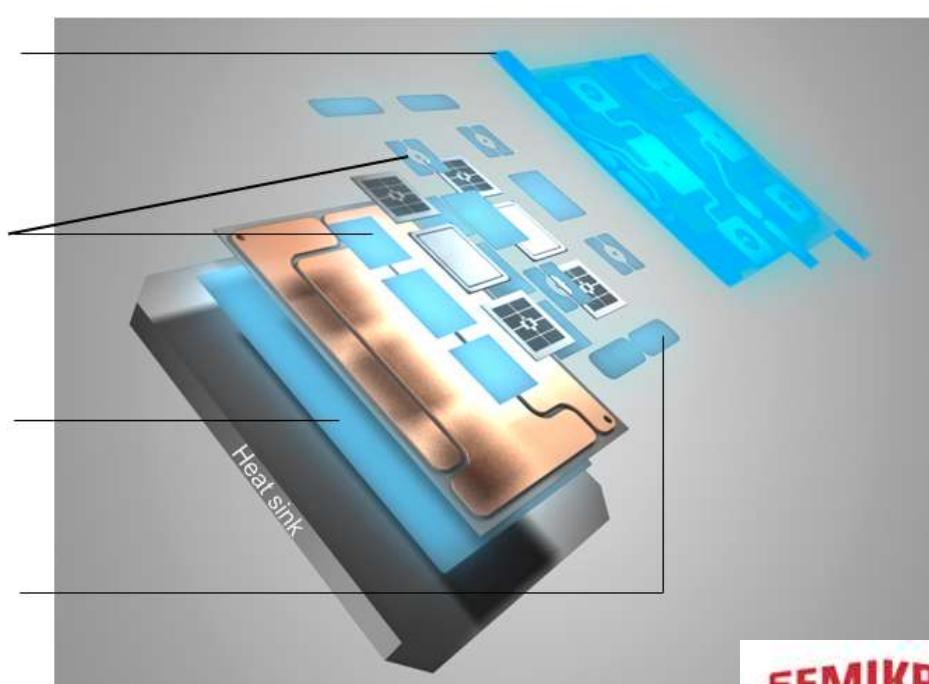


Development trends: Top-side bonding

Alternative top side interconnection technology

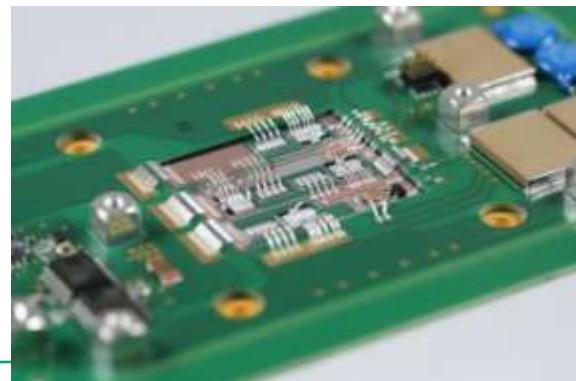
Low-temperature sintering with Ag powder or paste of metallic shapes and strands or flex material is in discussion and partial in production:

- The SKiN flex layer replaces the bond wires
- Chips are sintered on chip upper and underside
- The thermal paste layer is replaced by a sinter layer
- Terminals are sintered to the DBC



Courtesy of

SEMIKRON
innovation+service



THANK YOU VERY MUCH FOR YOUR ATTENTION

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